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				Application Number	10 / 765, 804
				Filing Date	1-27-04
				First Named Inventor	Paul A. Ingersoll
				Group Art Unit	2823
				Examiner Name	B. K. B. E.
Sheet	1	of	1	Attorney Docket Number	SC13113TP

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
Bu	AE	WATANABE et al., "An Advanced Technique for Fabricating Hemispherical-Grained (HSG) Silicon Storage Electrodes," IEEE TRANSACTIONS ON ELECTRON DEVICES, VOL. 42, NO. 2, FEBRUARY 1995, pp. 295-300.	
Bu	AF	DE BLAUWE et al., "A Novel, Aerosol-Nanocrystal Floating-Gate Device for Non-Volatile Memory Applications," IEEE, pp. 29.7.1 - 29.7.4 (2000).	

Examiner Signature	Brook Kessed	Date Considered	4/17/2005
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